

IST UNIT TEST 2017 – 18

SUB: VOC II

STD: XII

MARKS: 25

DATE: 11/08/17

TIME: 1Hr. 30 mins

Q.1.A] Select the correct alternative and rewrite the sentences: 4

1) _____ bus is one way data path from microprocessor to all devices.

a) datab) addressc) USBd) MCA

2) ALU is _____ bit unit in 8085 microprocessor.

a) 8b) 16c) 5d) 4

3) _____ is the three byte instruction.

a) STA 2003Hb) MVI 04Hc) MOV A,Dd) none of these

4) _____ instruction is used to load accumulator indirectly.

a) STAb) LDAc) LDAXd) STAX

B] Answer any two of the following:

6

1) Write a note on evolution of microprocessor with examples.

2) Explain Instruction cycle , machine cycle and T - state

3) Differentiate between hardware and software interrupts.

Q.2.A] Answer any two of the following:

6

1) Explain any two pins of 8085 microprocessor.

2) If [A]=05H and [B]=02H. What would be the result on execution of ADD B instruction? Also show the flag changes.

3) Explain any two addressing modes of 8085 microprocessor.

B] Answer any one of the following:

4

1) Draw a labeled internal block diagram of 8085 microprocessor and explain any three blocks.

2) Explain LHLD and SHLD instructions with proper diagram.

Q.5] Answer the following:

5

1) Write an ALP to subtract two 8-bit numbers stored at memory locations 3001H and 3002H. Store result at 3003H.

OR

2) Suppose two 8-bit numbers are stored at memory locations C001H and C002H respectively. Write an ALP to add these two 8-bit numbers and store the result at memory location C003H.